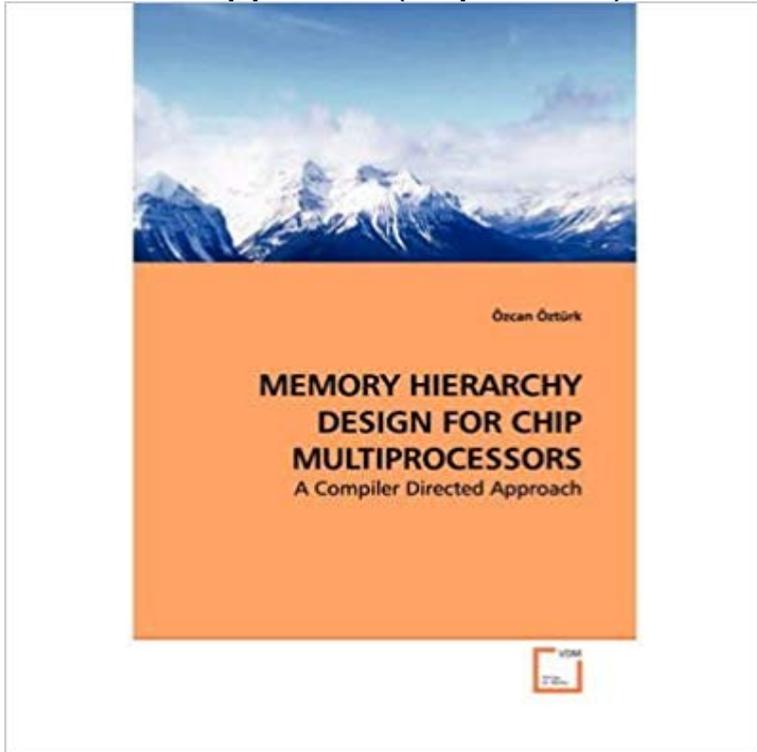# Memory Hierarchy Design for Chip Multiprocessors - A Compiler Directed Approach (Paperback) - Common

This book presents memory hierarchy design and data access management methodology. First, memory hierarchy design and data allocation problem is discussed, followed by a compiler-driven approach to data compression for reducing memory space occupancy. Third, compiler supported loop-data optimization problem to improve locality of data accesses for a given application code is discussed. Since paral...

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